TESTING AND RESULTS

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The first step in our testing phase was to load small instruction sets to the micro controller to verify proper wiring of the board and tested for certain operation such as power, saving data, detecting movement. The testing involved verifying all individual components of our manufactured GPS stack powers up correctly and individual paths are correct in the way we envisioned. Once that phase was completed, we moved into verification logic of accelerometer code that enables and disables GPS data acquisition. One of the major aspects of our operational sequence was the transfer of data from front-end GPS unit to our internal memory and finally outputting these data using serial interface. After we verified the individual modules to be working, we combined them together to ultimately build a single overall operational code for our GPS stack. Extended testing over the GPS stack with its associated results remains the one of the primary objectives of this project.

Socket Error

Voltage Regular Drop

JTAG